

Electronic Acknowledgement Receipt

EFS ID:	1568785
Application Number:	09919372
International Application Number:	
Confirmation Number:	2525
Title of Invention:	Clock distribution circuits and methods of operating same that use multiple clock circuits connected by phase detector circuits to generate and synchronize local clock signals
First Named Inventor/Applicant Name:	Vadim Gutnik
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Filer:	David Moore/Sheena Donnelly
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	5347-205amendment.pdf	569984	no	9

Warnings:

Information:	
Total Files Size (in bytes):	569984
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